

Fig. 2

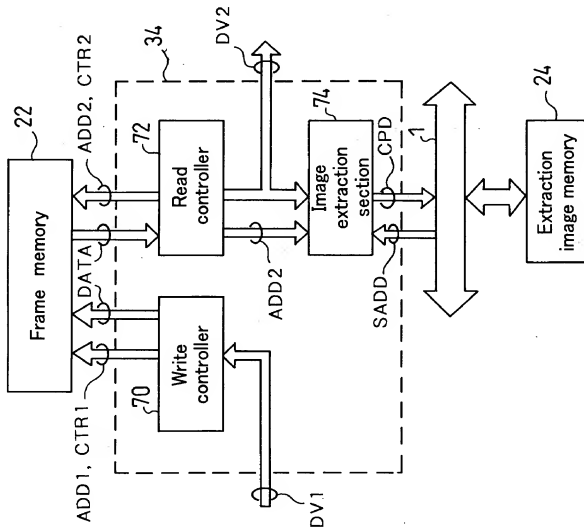


Fig. 3

Fig. 4 (a)

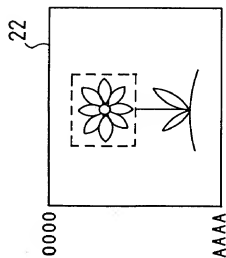


Fig. 4 (b)

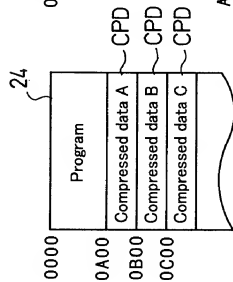


Fig. 4 (c)

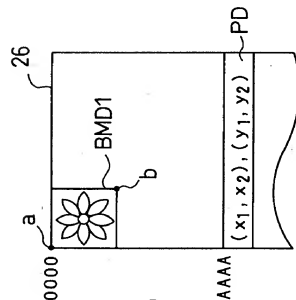


Fig. 4 (d)

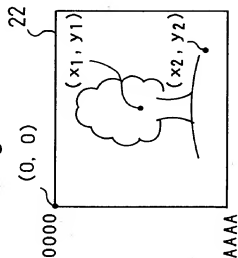
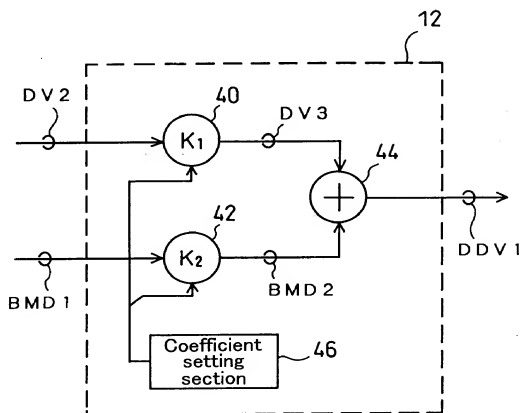


Fig. 5



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Fig. 6 (a)

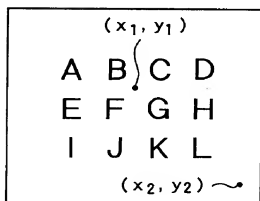


Fig. 6 (c)

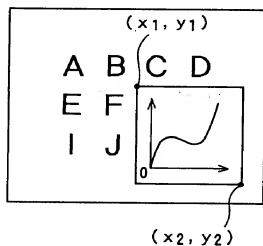


Fig. 6 (b)

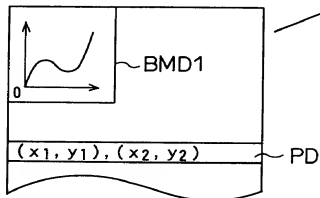


Fig. 7

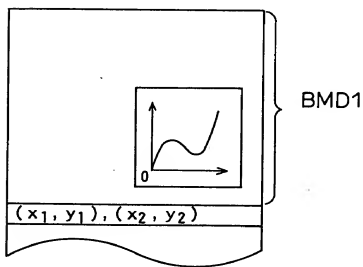


Fig. 8 (a)

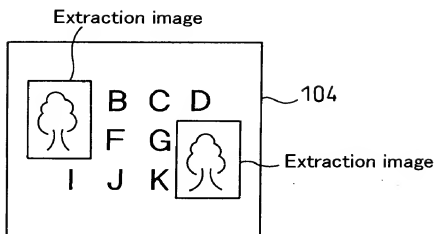


Fig. 8 (b)

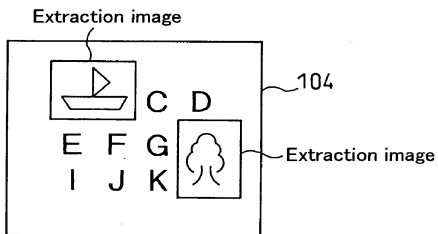


Fig. 8 (c)

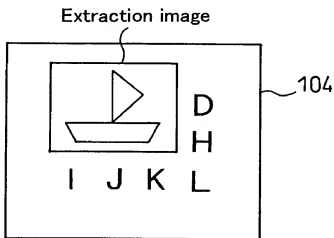


Fig.9

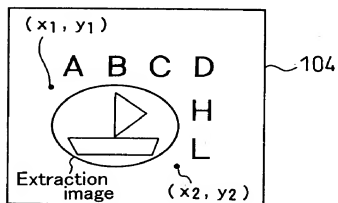


Fig.10

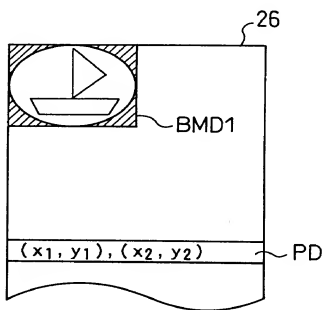


Fig. 11(a)

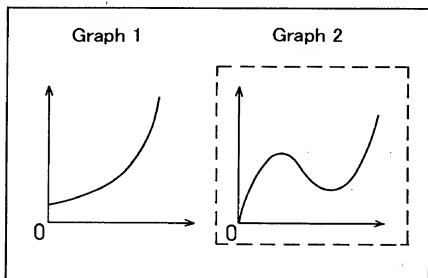
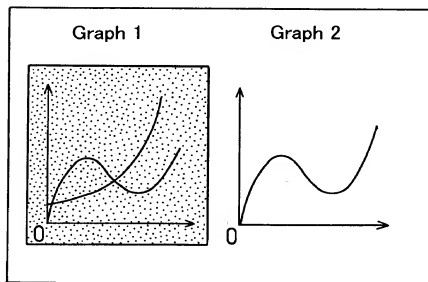


Fig. 11(b)



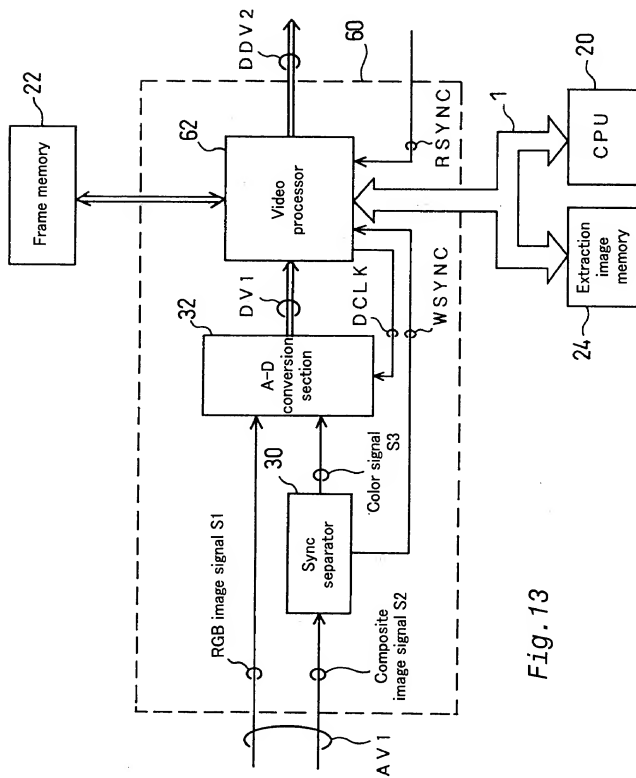
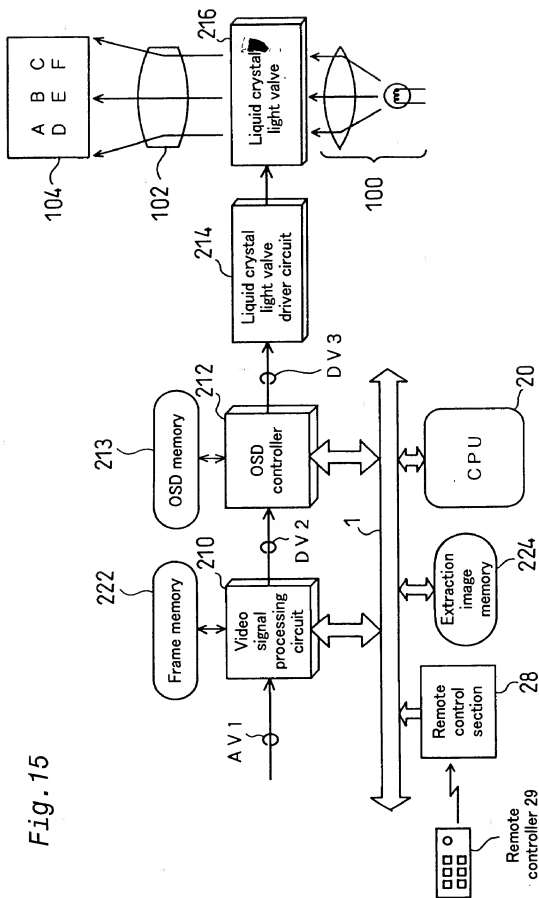
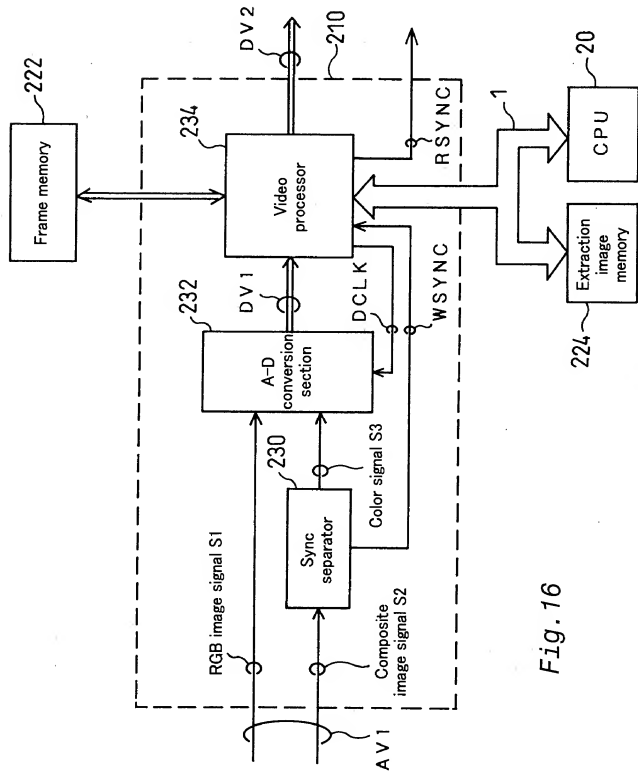


Fig. 15





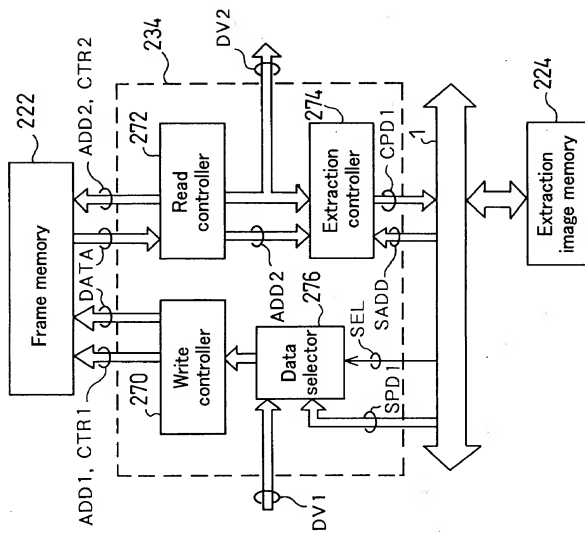
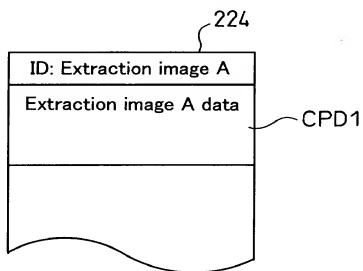


Fig. 17

Fig. 18



00000000.000000

Fig. 19

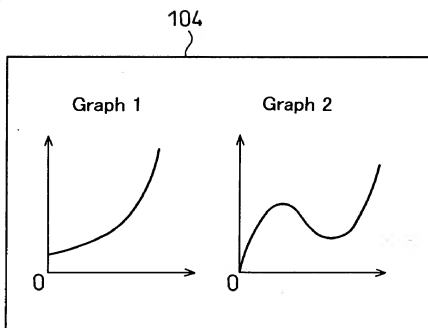


Fig. 20

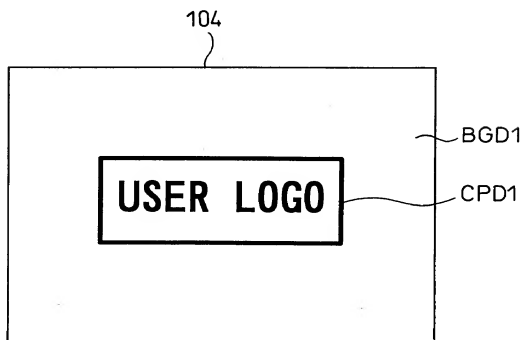


Fig. 21(a)

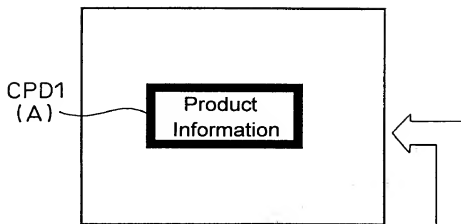


Fig. 21(b)

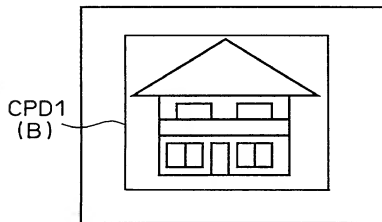
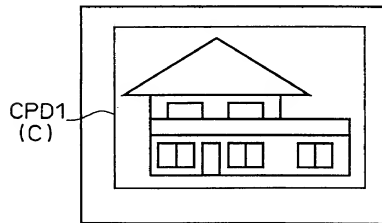


Fig. 21(c)



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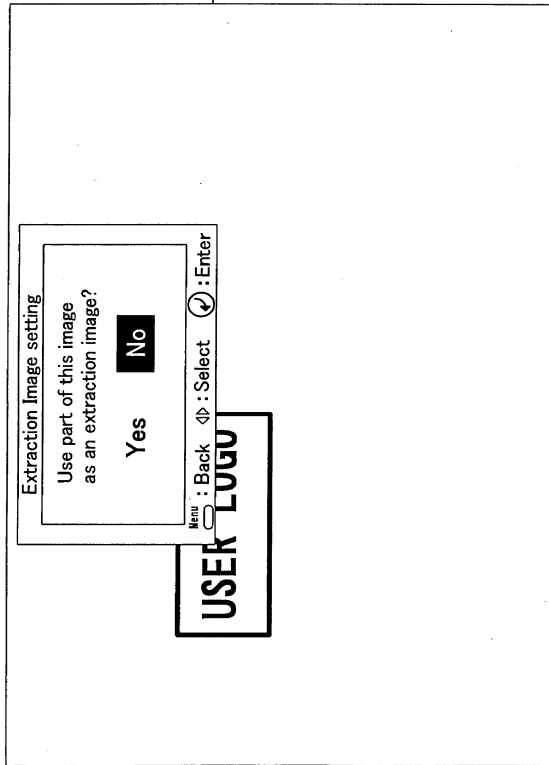


Fig.22

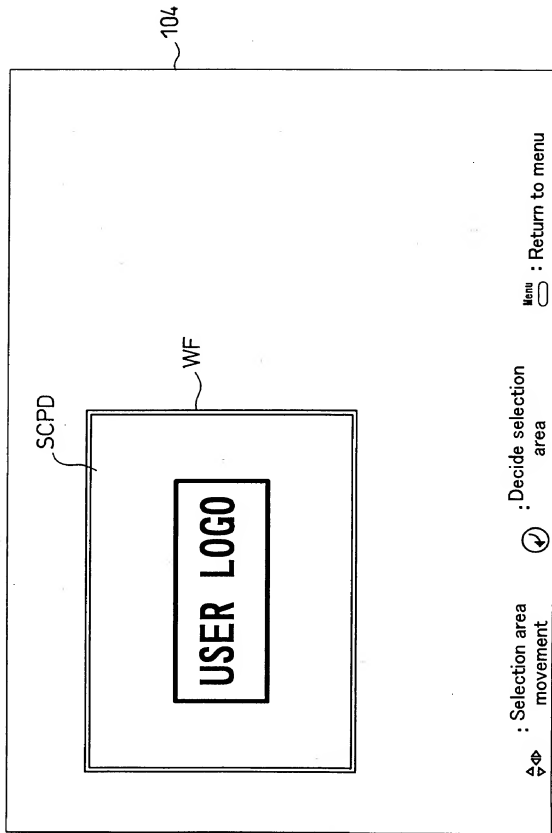


Fig. 23

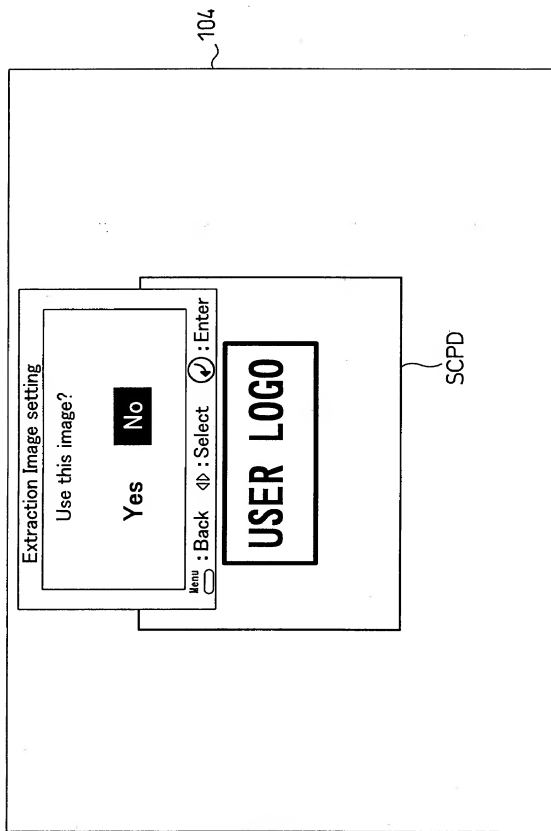


Fig. 24

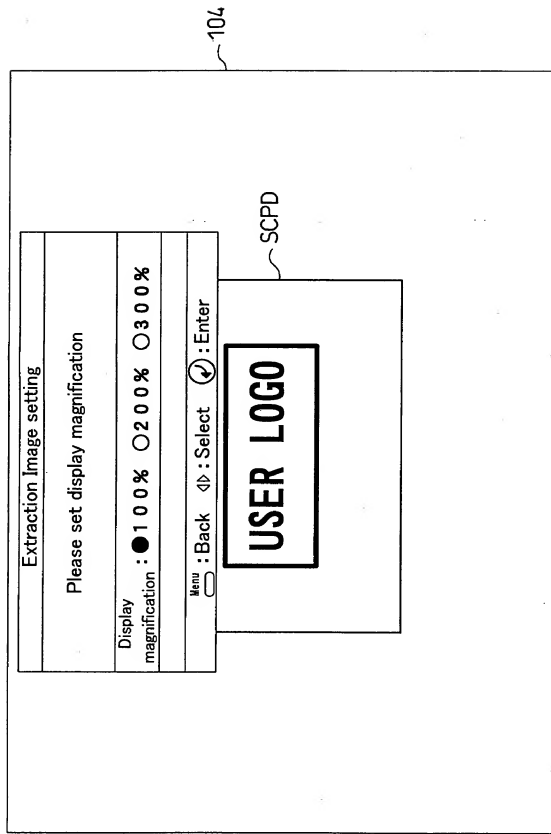


Fig.25

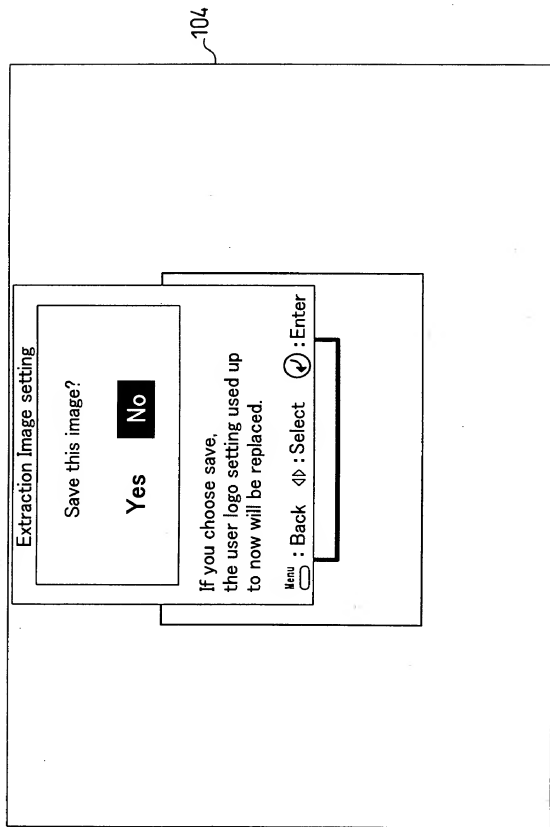


Fig. 26

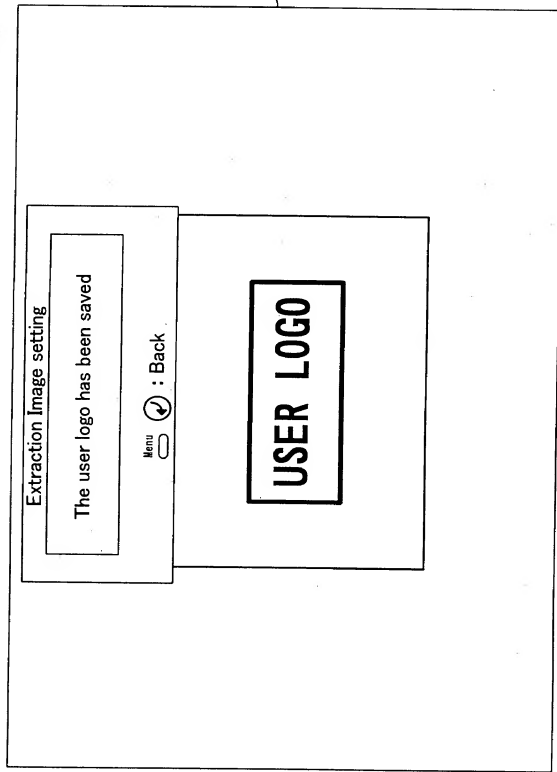


Fig. 27

Fig.28

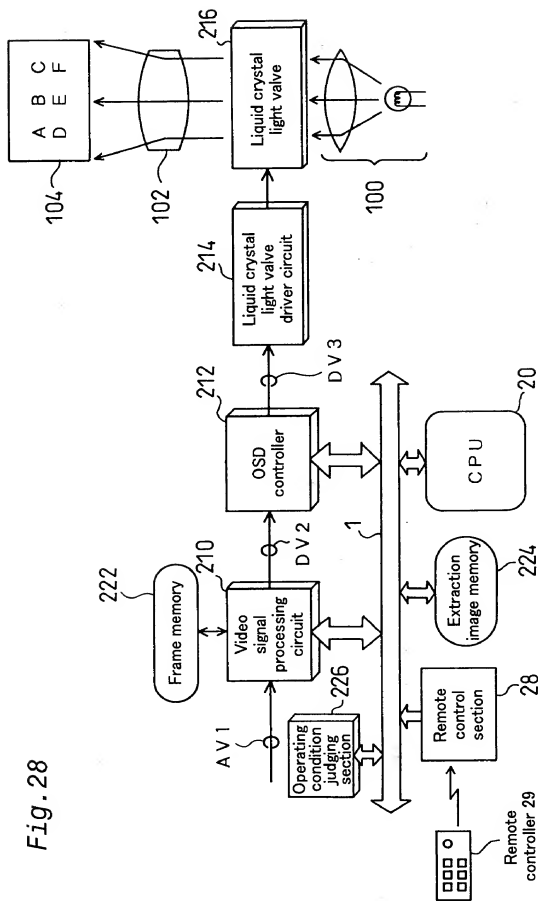


Fig. 29

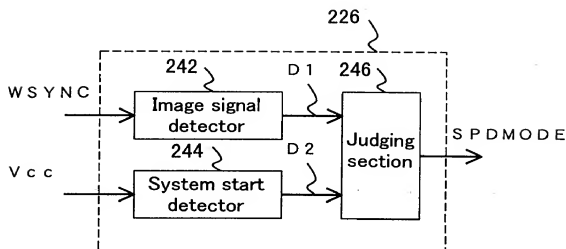


Fig. 30(a)

 V_{CC}

Fig. 30 (b)

D 2

Fig. 30 (c)

WSYNC

Fig. 30 (d)

D 1

Fig. 30 (e)

SPDMODE

